



1 Inventor: Pai-Hung Pan

2 Title: Semiconductor Processing Methods of Forming a Conductive  
3 Gate and Line

4 Assignee: Micron Technology, Inc.

5 Serial No.: 09/059,644

6 Filed: April 13, 1998

7 **INFORMATION DISCLOSURE STATEMENT**

8 **PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98**

9 In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, the Examiner's  
10 attention is directed to the references listed on the attached Form PTO-1449  
11 and copies of which are attached. No admission is made regarding whether  
12 all the submitted references are prior art.

13 Citation of these patents is respectfully requested.

14  
15 Respectfully submitted,

16  
17 Date: Apr 18, 2000

18 Attorney: Bernard Berman

19 Bernard Berman  
20 Reg. No. 37,279  
21 Wells, St. John, Roberts,  
22 Gregory & Matkin, P.S.

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